

# Electronic Circuit Design & the EDA Toolchain

By Slugbotics

The electronics circuit design module of CRSN-151C will introduce the basic concepts of electricity, circuit design, and circuit building by taking participants through the full manufacturing process of a printed circuit board. Students will initially learn the physics and measurement skills needed to design and troubleshoot their circuit on a breadboard before working through the entire Electronic Design Automation workflow to build a printed circuit board that controls the speed and direction of a motor. In addition, Git version control and the use of soldering for circuit construction are introduced. Taking this class will prepare you to streamline your own personal electronics projects and give you the skills to participate in Slugbotics, UCSC Rocket team, and/or Formula Slug.

## Learning Objectives

- Become familiar with electricity, current, voltage, and resistance.
- Be able to perform basic circuit analysis using Kirchhoff's Current and Voltage Laws.
- Observe circuit and component behavior using multimeters and oscilloscopes.
- Demonstrate professional engineering, communication, collaboration, and documentation practices through the use of lab notebooks and peer review.
- Source the appropriate electrical components from online references for a buck converter.
- Understand datasheets for electronic components.
- Demonstrate familiarity with the entire Electronic Design Automation (EDA) workflow by designing a system to control the speed and direction of a motor.
- Perform version control using Git to keep track of EDA files.
- Find or create EDA models and symbols.
- Gain familiarity with EAGLE PCB design.
- Become proficient with soldering and desoldering.

# Synchronous Classes

This class is taught entirely in-person. Attending class and in-class participation are strongly encouraged. Lectures will be recorded, but most labs require special equipment that is only accessible in the lab.

Classes will be taught in two locations: E2 581 (computer lab) and E2 585.

# Contact Information

Jack Lin  
Teaching and Course Design  
@jlin143  
jlin143@ucsc.edu

Eliot Wachtel  
Teaching and Course Design  
@ewachtel  
ewachtel@ucsc.edu

Jake Nations  
Teaching and Course Design  
@jnations  
jnations@ucsc.edu

Alex Nivelles  
Course Design

# Office Hours

Office hours are offered weekly at regularly scheduled times by appointment on canvas. Sign up using the Canvas calendar. Office hours may or may not be recorded depending on staff preference.

Lab check-offs (if not completed in class) can be completed during office hours. Sign up for these as you would any office hour: by canvas appointment.

In some cases, there will be extended lab hours held in the lab. Students may remotely join these office hours via Zoom, or drop by in person for extra lab time. Check Canvas for the schedule of extended lab hours.

If none of the scheduled times work for you, please message the instructors to set up a time to meet.

Informal discussions will take place on slack, this includes: questions about assignments, project discussion, team formation, etc.

**Slack Channel: 151c-edacircuit**

## Grading and Assignments

This class has 10 lab projects that will be completed over ten weeks, as well as pre-lab activities (e.g quizzes) due most weeks<sup>1</sup>. You must keep a physical paper engineering notebook, which you will scan and submit at regular intervals. All project-related exercises are to be recorded in the laboratory notebook and submitted on Canvas as part of submitting the labs. Quizzes are due by the specified deadline; see the late policy for more information.

Lectures will be recorded, however, labs are required to be done in-person. If you cannot attend a lab or open lab session, please contact the teaching staff ahead of time to schedule an alternative time to access the lab space and materials.

### Grade Distribution:

**Labs** are graded based on the completion of the yellow-highlighted tasks in the lab manual. Physical labs will be turned in directly while computer-based labs will be turned in via the Sustainability Lab Gitlab repository. Commit IDs must also be submitted on Canvas.

---

<sup>1</sup> Quizzes will generally be assigned on Canvas and will be due the following Tuesday before lab

**Quizzes** are graded directly on Canvas. You will be given two attempts per quiz up until the due date. Feedback for free-response questions may take additional time for the teaching team to grade.

**Engineering Notebooks** and content will be graded for adherence to the handout guidelines linked below. We advise that you write notes in your notebook about lectures each week as well as ideas or problems you encounter during quizzes and labs. You can find some guidance on how to use your notebook in the resources linked below:

- How to keep a notebook:
- Standards of good engineering practice:
- Technical documentation:

This class is graded on a point system. Points are earned by completing assignments, notebook check-ins, and other activities.

The rough breakdown of graded assignments are as follows:

Total: 100%

Failure to meet the minimum requirements for any of the labs will lead to a failing grade in the class.

## Late Policy

Prelabs and quizzes may not be turned in late. The final lab and accompanying assignment may not be turned in late. During the quarter, Labs and the associated notebook content may be turned in late up until November 30 (Tuesday of week 10) for an immediate penalty of one half letter grade on the assignment and an additional full letter grade per week late after the due date. Notebook check-ins are subject to the same late policy as the labs.

## Course Requirements and Recommendations

A lab notebook is required for this class. Lab notebooks should be graph ruled and at least 5" by 7".

A personal laptop or desktop computer is recommended for this class. Please ensure that the computer is compatible with [Autodesk EAGLE](#).

- Slack: For class announcements and asynchronous communication.
- Git on a personal computer: For submitting computer-based labs.
- Zoom: For remote office hours and as a fallback lecture method.

Camera: For scanning and submitting notebook check-ins.

If your personal computer does not satisfy the requirements, please reach out to a student-instructor and we will provide alternative arrangements.

## Schedule

Week	Content	Tasks
0 9/23	Thursday: <<E2-585>> <ul style="list-style-type: none"> <li>• Joint meeting</li> <li>• Class intro and overview</li> </ul>	<ul style="list-style-type: none"> <li>• Select section to enroll in</li> <li>• Obtain a graph ruled lab notebook</li> <li>• Join Slack for communication</li> </ul>
1 9/28 9/30	Tuesday: <<E2-585>> <ul style="list-style-type: none"> <li>• Detailed class intro</li> <li>• Syllabus</li> <li>• Lab notebook and documentation</li> <li>• Lab safety</li> </ul> Thursday: <<E2-585>> <ul style="list-style-type: none"> <li>• Electrical Physics</li> <li>• Electrical Theory</li> <li>• Components</li> </ul>	<ul style="list-style-type: none"> <li>• Set up engineering notebook</li> <li>• Start safety training (Getting Started with S-Lab)</li> <li>• Prelab 1 assigned, due Tuesday next week</li> </ul>
2 10/05 10/07	Tuesday: <<E2-585>> <ul style="list-style-type: none"> <li>• Basic electrical circuits</li> <li>• Equivalent Circuits</li> </ul>	<ul style="list-style-type: none"> <li>• Prelab 1 due this Tuesday</li> <li>• Lab 1 due before 10/12</li> </ul>

	<ul style="list-style-type: none"> <li>• Intro to the Multimeter</li> <li>• Lab 1: Component Behavior</li> </ul> <p>Thursday: &lt;&lt;E2-585&gt;&gt;</p> <ul style="list-style-type: none"> <li>• Advanced components</li> <li>• Circuit analysis (KVL &amp; KCL)</li> <li>• Voltage divider</li> <li>• Circuit simulators</li> </ul>	<ul style="list-style-type: none"> <li>• Prelab 2 assigned, due Tuesday next week</li> </ul>
3 10/12 10/14	<p>Tuesday: &lt;&lt;E2-585&gt;&gt;</p> <ul style="list-style-type: none"> <li>• Intro to the Oscilloscope</li> <li>• Breadboards</li> <li>• RC RL RLC circuits</li> <li>• Buck converter circuit</li> </ul> <p>Thursday: &lt;&lt;E2-585&gt;&gt;</p> <ul style="list-style-type: none"> <li>• Lab 2: Circuit Testing</li> </ul>	<ul style="list-style-type: none"> <li>• Prelab 2 due this Tuesday</li> <li>• Lab 2 due before 10/19</li> <li>• Prelab 3 assigned, due Tuesday next week</li> <li>• Safety training due this Thursday</li> </ul>
4 10/19 10/21	<p>Asynchronous:</p> <ul style="list-style-type: none"> <li>• Command line</li> <li>• Git</li> <li>• Lab 4: Git Repositories</li> </ul> <p>Tuesday: &lt;&lt;E2-585&gt;&gt;</p> <ul style="list-style-type: none"> <li>• Soldering theory</li> <li>• Soldering demo</li> </ul> <p>Thursday: &lt;&lt;E2-581&gt;&gt;</p> <ul style="list-style-type: none"> <li>• Soldering review</li> <li>• Lab 3: Soldering Techniques</li> </ul>	<ul style="list-style-type: none"> <li>• Prelab 3 due this Tuesday</li> <li>• Lab 3 due before 10/26</li> </ul>
5 10/26 10/28	<p>Tuesday: &lt;&lt;E2-581&gt;&gt;</p> <ul style="list-style-type: none"> <li>• Autodesk EAGLE</li> <li>• Libraries</li> <li>• Lab 5: First Schematic</li> </ul> <p>Thursday: &lt;&lt;E2-581&gt;&gt;</p> <ul style="list-style-type: none"> <li>• Board view</li> <li>• Creating libraries in EAGLE</li> <li>• Lab 6 practice</li> </ul>	<ul style="list-style-type: none"> <li>• Lab 4 &amp; 5 due Sunday 10/31</li> <li>• Prelab 4&amp;5 due this Tuesday</li> <li>• Prelab 6 assigned, due Tuesday next week</li> </ul>

<p>6 11/02 11/04</p>	<p>Tuesday: &lt;&lt;E2-581&gt;&gt;</p> <ul style="list-style-type: none"> <li>● Component search &amp; selection</li> <li>● Bill of materials (BoM)</li> <li>● Lab 6: Buck Converter Schematic</li> </ul> <p>Thursday: &lt;&lt;E2-581&gt;&gt;</p> <ul style="list-style-type: none"> <li>● Turn schematic into a PCB <ul style="list-style-type: none"> <li>○ Board view</li> <li>○ Placement</li> <li>○ Gerber file</li> </ul> </li> <li>● Board manufacture</li> <li>● Demo arranging components in board view</li> </ul>	<ul style="list-style-type: none"> <li>● Prelab 6 due this Tuesday</li> <li>● Lab 6 due before 11/09</li> <li>● Prelab 7 assigned, due Tuesday next week</li> </ul>
<p>7 11/09 11/11</p>	<p>Tuesday: &lt;&lt;E2-581&gt;&gt;</p> <ul style="list-style-type: none"> <li>● Lab 7: Buck Converter Board</li> </ul> <p>Thursday: &lt;&lt;HOLIDAY&gt;&gt;</p> <ul style="list-style-type: none"> <li>● Holiday (Veteran's Day)</li> </ul>	<ul style="list-style-type: none"> <li>● Prelab 7 due this Tuesday</li> <li>● Lab 7 due before MIDNIGHT.</li> <li>● Prelab 8 assigned, due Tuesday next week</li> </ul>
<p>8 11/16 11/18</p>	<p>Tuesday: &lt;&lt;E2-581&gt;&gt;</p> <ul style="list-style-type: none"> <li>● Transistors</li> <li>● Relays</li> <li>● H-bridges</li> <li>● Switches</li> <li>● Lab 8: Motors and H-bridge</li> </ul> <p>Thursday: &lt;&lt;E2-581&gt;&gt;</p> <ul style="list-style-type: none"> <li>● Intro to ICs</li> <li>● 555 Timer and PWM</li> </ul>	<ul style="list-style-type: none"> <li>● Prelab 8 due this Tuesday</li> <li>● Lab 8 due before 11/23</li> <li>● Prelab 9 assigned, due Tuesday next week</li> <li>● Assignment: Convert H-bridge to schematic and board</li> </ul>
<p>9 11/23 11/25</p>	<p>Tuesday: &lt;&lt;E2-581&gt;&gt;</p> <ul style="list-style-type: none"> <li>● Review 555 Timer IC</li> <li>● Lab 9: LED control</li> </ul> <p>Thursday: &lt;&lt;HOLIDAY&gt;&gt;</p> <ul style="list-style-type: none"> <li>● Holiday (Thanksgiving)</li> </ul>	<ul style="list-style-type: none"> <li>● Prelab 9 due this Tuesday</li> <li>● Lab 9 due before 11/30</li> <li>● Prelab 10 assigned, due Tuesday next week</li> </ul>
<p>10 11/30</p>	<p>Tuesday: &lt;&lt;E2-585&gt;&gt;</p>	<ul style="list-style-type: none"> <li>● Prelab 10 (exit survey) due this Tuesday</li> </ul>

12/02	<ul style="list-style-type: none"> <li>• Lab 10: Assembly of Motor Control System</li> </ul> <p>Thursday: &lt;&lt;E2-585&gt;&gt;</p> <ul style="list-style-type: none"> <li>• Lab 10 extra time</li> <li>• Extra topics</li> <li>• Peer review for final assignment</li> </ul>	<ul style="list-style-type: none"> <li>• Lab 10 due at the end of class on Thursday.</li> <li>• Final assignment assigned, due 12/08 @ 10:30 PM</li> </ul>
Finals week	Final assignment instead of final exam	<ul style="list-style-type: none"> <li>• Final assignment due by Dec 8 @ 10:30 PM</li> </ul>